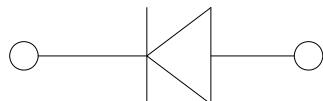
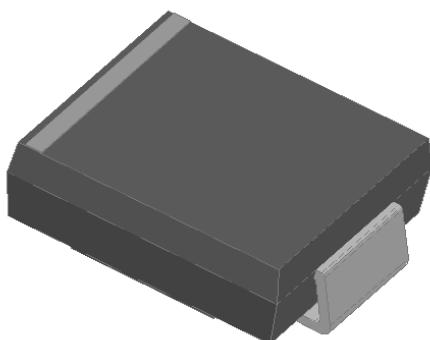


## Surface Mount General Purpose Rectifier



### Features

- Low profile package
- Ideal for automated placement
- Glass passivated chip junction
- High forward surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

### Typical Applications

For use in general purpose rectification of power supplies, inverters, converters, and freewheeling diodes for consumer, and telecommunication.

### Mechanical Data

- Package: DO-214AB (SMC)  
Molding compound meets UL 94 V-0 flammability rating, RoHS-compliant, halogen-free
- Terminals: Tin plated leads, solderable per J-STD-002 and JESD22-B102
- Polarity: Color band denotes the cathode end

### ■ Maximum Ratings (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	S10A	S10B	S10D	S10G	S10J	S10K	S10M
Device marking code			S10A	S10B	S10D	S10G	S10J	S10K	S10M
Maximum Repetitive peak reverse voltage	V <sub>RRM</sub>	V	50	100	200	400	600	800	1000
Maximum RMS Voltage	V <sub>RMS</sub>	V	35	70	140	280	420	560	700
Maximum DC Blocking Voltage	V <sub>DC</sub>	V	50	100	200	400	600	800	1000
Average Rectified Output Current (@60Hz sine wave, Resistance load, TL (FIG.1))	I <sub>O</sub>	A				10			
Forward Surge Current (Non-repetitive) (@60Hz Half-sine wave, 1 cycle, T <sub>j</sub> =25°C)	I <sub>FSM</sub>	A				200			
Forward Surge Current (Non-repetitive) (@1ms, square wave, 1 cycle, T <sub>j</sub> =25°C)						400			
Current squared time (@1ms≤t≤8.3ms T <sub>j</sub> =25°C)	I <sup>2</sup> t	A <sup>2</sup> s				166			
Storage Temperature	T <sub>stg</sub>	°C				-55 ~ +150			
Junction Temperature	T <sub>j</sub>	°C				-55 ~ +150			

### ■ Electrical Characteristics (Ta=25°C Unless otherwise specified)

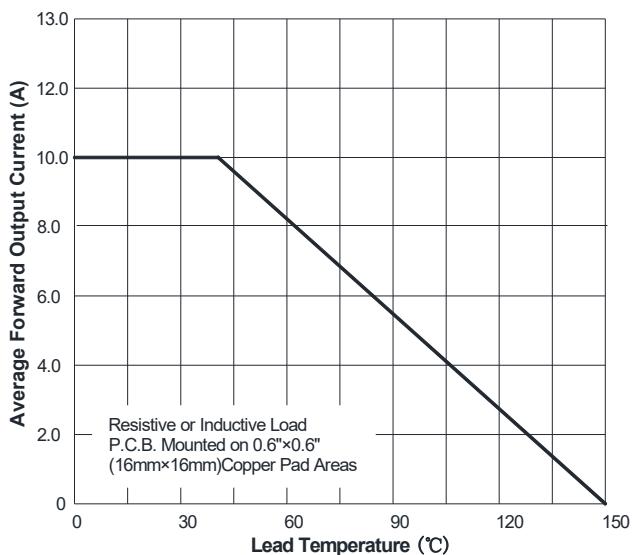
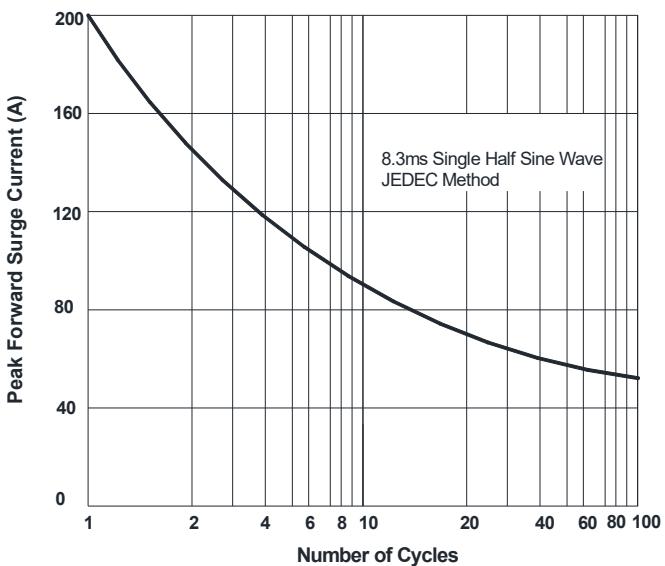
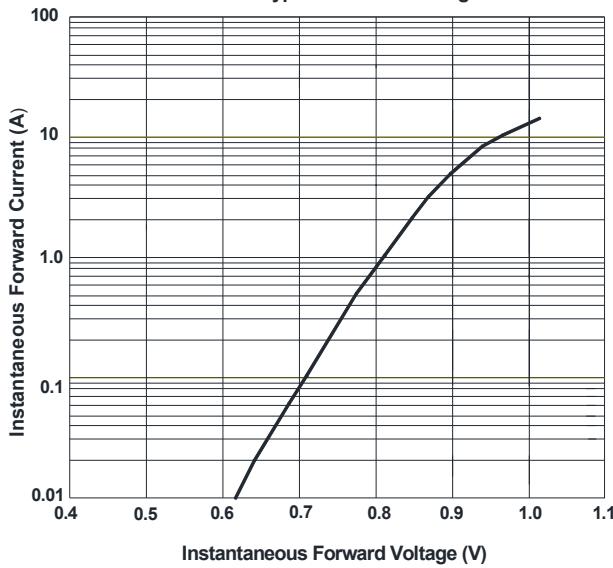
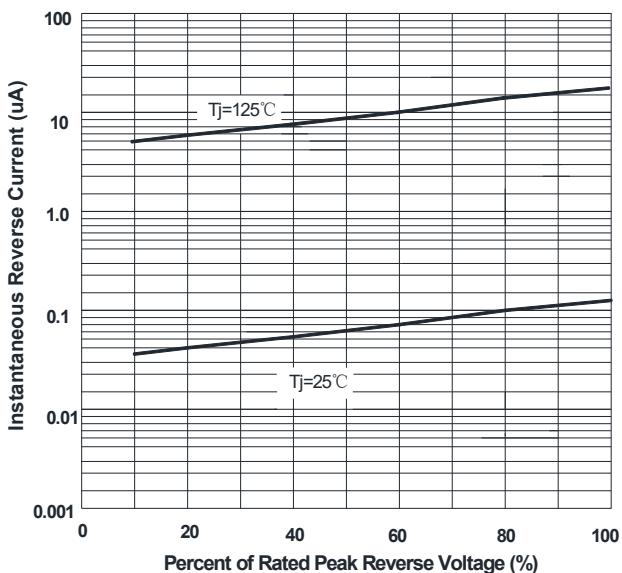
PARAMETER	SYMBOL	UNIT	TEST CONDITIONS	S10A	S10B	S10D	S10G	S10J	S10K	S10M
Maximum instantaneous forward voltage	V <sub>F</sub>	V	I <sub>FM</sub> =10A				1.1			
Maximum DC reverse current at rated DC blocking voltage	I <sub>R</sub>	μA	T <sub>j</sub> =25°C				5			
			T <sub>j</sub> =125°C				100			
Typical junction capacitance	C <sub>j</sub>	pF	Measured at 1MHz and Applied Reverse Voltage of 4.0 V.D.C				55			

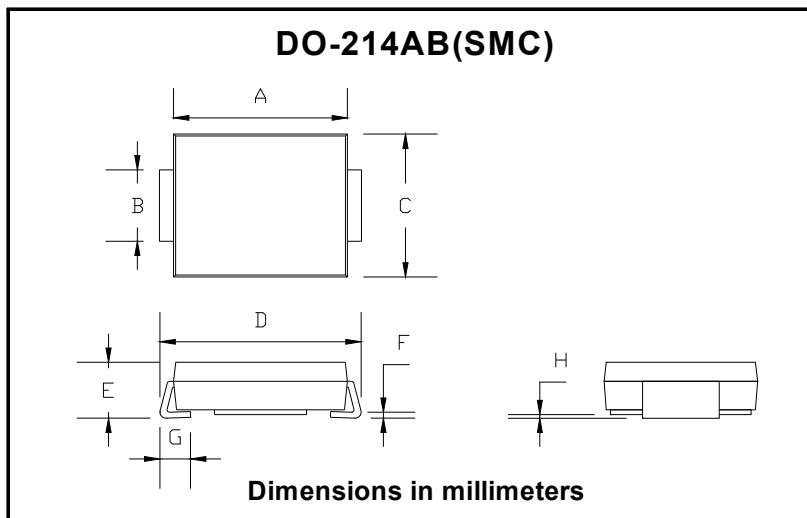
**■ Thermal Characteristics (Ta=25°C Unless otherwise specified)**

PARAMETER	SYMBOL	UNIT	S10A	S10B	S10D	S10G	S10J	S10K	S10M
Typical Thermal resistance	R <sub>θJ-A(1)</sub>	°C/W			50				
	R <sub>θJ-L(1)</sub>				10				
	R <sub>θJ-C(1)</sub>				8				

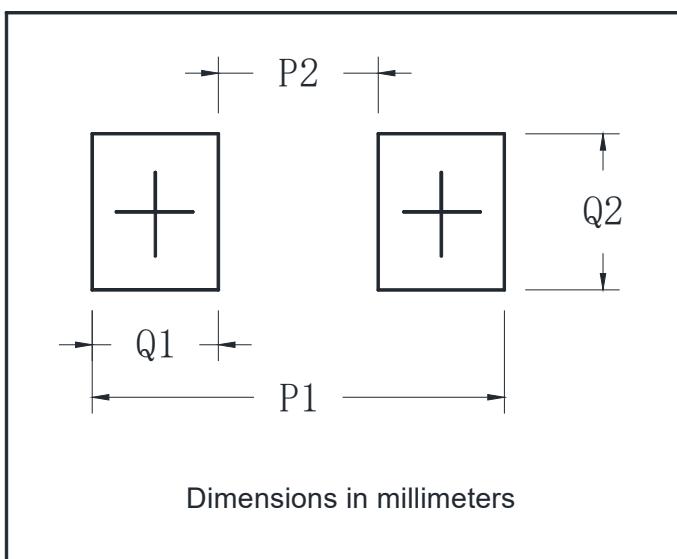
Note(1)

Thermal resistance from junction to ambient and from junction to lead mounted on P.C.B. with 0.6" x 0.6" (16 mm x 16 mm) copper pad areas

**■ Characteristics(Typical)**
**FIG.1: Io-TL Curve**

**FIG.2: Forward Surge Current Capability**

**FIG.3: Typical Forward Voltage**

**FIG.4: Typical Reverse Characteristics**


**■ Outline Dimensions**

DO-214AB (SMC)		
Dim	Min	Max
A	6.60	7.11
B	2.85	3.27
C	5.59	6.22
D	7.75	8.13
E	1.99	2.61
F	0.15	0.31
G	0.76	1.52
H	0.05	0.20

**■ Suggested pad layout**

DO-214AB (SMC)	
Dim	Min
P1	9.9
P2	3.84
Q1	3.03
Q2	3.82